

Original circuit built 2013
 Revisions Sept 2021
 Q3 fine bias adjustment

*** Note: For best results with the FET Q4, the ambient drain voltage should sit at around 5 → 5.5 volts, and the duty cycle should stay as even as possible in OD modes. This can be easily checked with a DVM by turning the signal on & off and checking that the DC voltage at the drain doesn't change by more than 200 mV

